

WHAT IS CLAIMED IS:

1. A drive circuit characterized by:
  - a plurality of current signal generation circuits for outputting a current signal to each of a plurality of output units;
  - a current signal output line to which outputs of said plurality of current signal generation circuits are commonly connected;
  - a control circuit for controlling each of said plurality of current signal generation circuits to be a current signal output state capable of evaluating an output of a one or more specific circuits of said plurality of current signal generation circuits on a basis of current values output through said current signal output line;
  - a correction value output circuit for evaluating the output of said one or more specific circuits of said plurality of current signal generation circuits on a basis of the current values output through said current signal output line to output a correction value according to an evaluation result; and
  - a correction circuit for correcting an image signal supplied to said current signal generation circuits by means of the correction value.
2. A drive circuit according to claim 1,

wherein said control circuit supplies a predetermined signal to said one or more specific circuits of said current signal generation circuits, and supplies a signal different from the predetermined signal to the  
5 other current signal generation circuits commonly.

3. A drive circuit according to claim 2,  
wherein the different signal is a signal such that a current value of a current signal output from each of  
10 the other or others of the current signal generation circuits, to which the different signal has been supplied, is sufficiently smaller than a current value of the current signal output from said one or more specific circuits of said current signal  
15 generation circuits.

4. A drive circuit characterized by:  
a plurality of current signal generation circuits for outputting a current signal to each of a  
20 plurality of output units;

a current signal output line to which outputs of said plurality of current signal generation circuits are commonly connected;

a correction value output circuit for  
25 outputting a correction value obtained by evaluating the output of a one or more specific circuits of said plurality of current signal generation circuits on a

basis of current values output through said current  
signal output line; and

a correction circuit for correcting an image  
signal supplied to said current signal generation  
5 circuits by means of the correction value.

5. A device circuit according to claim 1,  
further characterized by:

a switch for realizing a state in which said  
10 current signal output line is connected to said  
plurality of current signal generation circuits  
simultaneously.

6. A device circuit according to claim 4,  
15 further characterized by:

a switch for realizing a state in which said  
current signal output line is connected to said  
plurality of current signal generation circuits  
simultaneously.

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7. A drive circuit according to claim 1,  
further characterized by:

a plurality of switches for severally  
controlling connection relations between said  
25 plurality of current signal generation circuits and  
said current signal output line, said plurality of  
switches being controlled by a common control signal.

8. A drive circuit according to claim 4,  
further characterized by:

a plurality of switches for severally  
controlling connection relations between said  
5 plurality of current signal generation circuits and  
said current signal output line, said plurality of  
switches being controlled by a common control signal.

9. A drive circuit according to claim 1,  
10 further characterized by:

a plurality of switches for severally  
controlling connection relations between said  
plurality of current signal generation circuits and  
said plurality of output units, said plurality of  
15 switches being controlled by a common control signal.

10. A drive circuit according to claim 4,  
further characterized by:

a plurality of switches for severally  
20 controlling connection relations between said  
plurality of current signal generation circuits and  
said plurality of output units, said plurality of  
switches being controlled by a common control signal.

25 11. A drive circuit according to claim 1,  
wherein said drive circuit is a drive circuit for a  
display apparatus including display elements, and

said display apparatus includes at least a part of said display elements formed on a substrate on which said current signal generation circuits and said current signal output line are formed.

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12. A drive circuit according to claim 4, wherein said drive circuit is a drive circuit for a display apparatus including display elements, and said display apparatus includes at least a part of  
10 said display elements formed on a substrate on which said current signal generation circuits and said current signal output line are formed.

13. A drive circuit according to claim 1,  
15 wherein each of said current signal generation circuits includes at least a circuit for outputting a current signal having a squared value of a value of an input signal, and said correction value output circuit outputs a correction value obtained by  
20 calculating a square root of a ratio between an output evaluation value of said one or more specific circuits of said current signal generation circuits obtained by the evaluation and a reference value.

25 14. A drive circuit according to claim 8, wherein each of said current signal generation circuits includes at least a circuit for outputting a

current signal having a squared value of a value of  
an input signal, and said correction value output  
circuit outputs a correction value obtained by  
calculating a square root of a ratio between an  
5 output evaluation value of said one or more specific  
circuits of said current signal generation circuits  
obtained by the evaluation and a reference value.

15. A drive circuit according to claim 13,  
10 wherein said correction value output circuit includes  
a calculation circuit for calculating the square root,  
and the calculation is an approximation calculation  
performed by classifying according to a value of the  
ratio between the output evaluation value and the  
15 reference value.

16. A drive circuit according to claim 14,  
wherein said correction value output circuit includes  
a calculation circuit for calculating the square root,  
20 and the calculation is an approximation calculation  
performed by classifying according to a value of the  
ratio between the output evaluation value and the  
reference value.

25 17. A display apparatus characterized by:  
a drive circuit according to claim 1;  
a plurality of data lines connected to the

plurality of output portions of said drive circuit severally; and

a plurality of display elements connected to said plurality of data lines severally.

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18. A display apparatus characterized by:

a drive circuit according to claim 4;

a plurality of data lines connected to the plurality of output portions of said drive circuit severally; and

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a plurality of display elements connected to said plurality of data lines severally.

19. An evaluation method of a drive circuit including a plurality of current signal generation circuits for outputting current signals to each of a plurality of output units, characterized by the steps of:

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connecting outputs of said plurality of current signal generation circuits to a common current signal output line;

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controlling each of said plurality of current signal generation circuits to a current signal output state in which an output of one or more specific circuits of said current signal generation circuits can be evaluated on a basis of current values output through said current single output line; and

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evaluating an output of said one or more specific circuits of said current signal generation circuits on a basis of the current values output through said current single output line.